

III. REMARKS

1. Claims 1-7, 9-12, and 17-23 remain in the application. Claims 8, 13-16, and 24-27 have been cancelled without prejudice. Claims 1, 11, 17 and 18 have been amended.

Support for the amendments may be found in the specification, for example, on page 6, line 29 through page 7, line 8, and page 7, line 25 through page 8, line 2.

2. Claims 1, 2, 5-7, 9-12, 17-19 and 21-23 are not anticipated by Rader (US 5,867,140), under 35 USC 102(e).

Rader fails to disclose or suggest a changing means for changing the position of the first part of the display element at a first set interval, and for changing information displayed on the first part of the display element with update information from an application during energy conservation mode, as recited by claim 1, 11, and 17.

Rader also fails to disclose or suggest a display module arranged to change the position of the first part of the display element on the display element, to obtain update information, and to change information displayed on the first part of the display element with said update information, as recited by claim 18.

Rader discloses a circuit and a display system. The display system comprises a display panel having area on which pictures can be generated. A display control circuit controls the display panel in two operating modes. In a first operating mode only a partial display field of the display area is active and used to display pictures in order to restrict power consumption. In a second operating mode the entire display

area is active. The display panel is, for example, an LCD-panel (column 2, row 34) a mobile terminal (column 1, row 61). The first display mode is used while the mobile terminal cover is closed or after a processor has been inactive for a certain time. The second display mode is used while the cover is open. The processor controls a horizontal or a vertical driver, which control the voltage fed to the rows and columns of the display panel (see column 3, lines 53-63; column 7, line 50 through column 8, line 2).

Pixel fill bits may be added to a display feed queue used by the horizontal and vertical drivers in the first operating mode to indicate unused areas of the display. A pixel off signal is fed to the display element when encountering such pixel fill bits. Thus, the image that is generated can be located in any region of the display area (see column 7, line 40 through column 8, line 2). By selecting the rows and columns to be switched off using the pixel off signals, the partial display field may be placed in any region of the display area (see column 8, lines 29-32). Rader also discloses that the display system may change the location of the partial display field by switching of different columns and rows (see column 8, lines 58-65).

However, Rader fails to disclose the feature of *changing means for changing the position of the first part of the display element on the display element at a first set interval and for changing information displayed on the first part of the display element with update information from an application during energy conservation mode in claim 1 and the equivalent features in other independent claims.*

Rader does disclose that the location of the partial field may change (column 8, lines 58-65). Furthermore, Figure 4 of Rader discloses a state control and timing logic block (422). However, the timing logic block 422 functions to control the operation of the input switch 414 and output switch 420 such that the source of the image buffer for the screen is switched from the first display image buffer to the second display image in a synchronized manner (column 8, lines 50-54).

In other words, the timing block switches an image source from a buffer to a display. This is clearly not the same as changing information displayed on the first part of the display element with update information as recited by the present claims.

Regarding the Examiner's reply in the "Response to Arguments" section on page 2 of the action mailed on 31 January 2006, specifically referring to the feature of changing information displayed on the first part of the display, Applicants respectfully submit that the cited passage does not relate to the abovementioned feature at all. In fact, Rader explicitly discloses that the information displayed on the partial field does not change in column 7, lines 8-19 where Rader explicitly states that the contents of the FIFO memory 416 is recirculated in the partial display mode. Therefore, Applicants maintain that Rader fails to disclose or suggest this feature.

At least for these reasons, Applicants submit that Rader does not anticipate independent claims 1, 11, 17, and 18 and dependent claims 2, 5-7, 9, 10, 12, 19, and 21-23.

3. Claims 3, 4, and 20 are patentable over the combination of Rader in view of Stedman et al. (US 5,675,364, "Stedman") under 35 USC 103 (a).

Claims 3 and 4 depend from claim 1 and claim 20 depends from claim 18.

Stedman fails to supply the features of claim 1 missing from Rader, that is, a changing means for changing the position of the first part of the display element at a first set interval, and for changing information displayed on the first part of the display element with update information from an application during energy conservation mode.

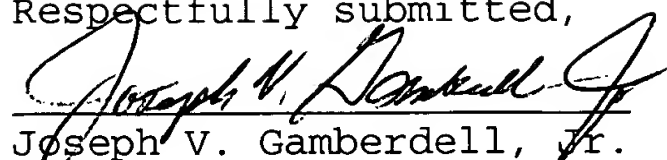
In addition, Stedman fails to disclose or suggest a display module arranged to change the position of the first part of the display element on the display element, to obtain update information, and to change information displayed on the first part of the display element with said update information, as recited by claim 18.

Thus, the combination of Rader and Stedman fails to disclose or suggest all the features of claims 1 and 18 and fails to render claims 3, 4, and 20 unpatentable.

For all of the foregoing reasons, it is respectfully submitted that all of the claims now present in the application are clearly novel and patentable over the prior art of record, and are in proper form for allowance. Accordingly, favorable reconsideration and allowance is respectfully requested. Should any unresolved issues remain, the Examiner is invited to call Applicants' attorney at the telephone number indicated below.

The Commissioner is hereby authorized to charge payment for any fees associated with this communication or credit any over payment to Deposit Account No. 16-1350.

Respectfully submitted,


Joseph V. Gamberdell, Jr.

14 April 2006
Date

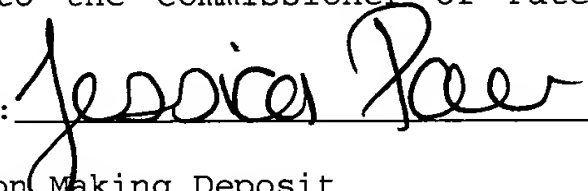
Reg. No. 44,695

Perman & Green, LLP
425 Post Road
Fairfield, CT 06824
(203) 259-1800
Customer No.: 2512

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date indicated below as first class mail in an envelope addressed to the Commissioner of Patents, P.O. Box 1450, Alexandria VA 22313-1450.

Date: 4.14.2006

Signature: 

Person Making Deposit